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DESCRIPTION

CAPACITOR

Technical Field [0001]

The present invention relates to a dielectric material and a capacitor, particularly a capacitor for use as an electronic circuit incorporated in printed boards or integrated circuits.

Background Art

[0002]

Recently, size, thickness, and weight of electric apparatuses have been reduced, electric circuits have been densified and digitalized, and therewith there have been increasing demand of improving size, performance, and reliability of electronic components. Under such circumstances, also capacitors are required to have small size and high capacity.

[0003]

However, surface mounted components such as the capacitors still occupy large area of printed circuit boards.

This is a major obstacle in further miniaturizing the electronic apparatuses. To overcome the problem, there have

been attempts to incorporate the electronic parts such as capacitors in circuit boards (e.g. Patent Documents 1 and 2).
[0004]

As frequency is increased and voltage is lowered in integrated circuits, malfunction caused by change of power supply voltage due to noise is becoming a serious problem. The problem is caused because allowable range of the power supply voltage is reduced with lowering the voltage. To prevent malfunction due to high-frequency noise, generally capacitors are placed around power supplies. The capacitors for the use are called bypass capacitors or decoupling capacitors, and act to remove the high-frequency noise and to prevent instantaneous reduction of the power supply voltage by supplying energy instantaneously from the capacitor. Electrostatic capacity of the capacitor is important for the energy supply.

Though an ideal capacitor has only electrostatic capacity components without resistance components and inductance components, practical capacitors have series resistance components and series inductance components. Impedance of the capacity components is reduced and the inductance components are increased as the frequency is increased. Therefore, with future increase of operating frequency, the inductance components of devices and the inductance components of wirings are expected to cause noise.

Thus, it is required that the capacitor has inductance components that are small as much as possible and a higher self-resonant frequency to certainly act over high frequency area. Further, to reduce the inductance components due to the wiring distance as much as possible, the nearer to CPU the decoupling capacitor is, the better.

[0006]

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On the other hand, with the above-described lowering of the power supply voltage, the rated working voltage of the capacitor will be able to be smaller in future. [0007]

To respond to the above problems of high frequency and low voltage of the integrated circuits, proposed are methods of embedding a high-performance capacitor in a printed circuit board to minimize the wiring distance between a CPU and the capacitor (e.g., Patent Documents 3 to 5), and methods of forming a capacitor into a thin film and incorporating the film in a power supply IC to form 1 chip device.

Further, in small-sized portable devices typified by mobile phones, printed circuit boards mainly comprise resin substrates, so that a capacitor having a little flexibility like the resin substrates and excellent high frequency properties has been strongly demanded.

[0009]

On the other hand, in most of disclosed proposals, dielectric materials are such that a ceramics-based material requiring high-temperature burning is embedded in a ceramic substrate (e.g., Patent Documents 6 and 7).

Patent Document 1: JP-A-10-56251

Patent Document 2: JP-A-11-68321

Patent Document 3: JP-A-4-211191

Patent Document 4: JP-A-10-335178

Patent Document 5: JP-A-11-111561

Patent Document 6: JP-A-8-222656

Patent Document 7: JP-A-8-181453

Disclosure of the Invention

Problems that the Invention is to Solve
[0010]

The dielectric materials, which comprise the ceramics-based material requiring high-temperature burning, embedded in the ceramic substrate, have problems. The ceramics-based material is likely to be broken, peeled, or contaminated in an industrial production process, and thereby is easily deteriorated. In particular, the material is often cracked in the electrode forming process including paste application and mounting, to cause defect of device properties.

Further, in the case of incorporating a capacitor in a resin substrate, a ceramics paste, which is converted to a dielectric material by burning, and the resin substrate cannot be burned together at a high temperature after forming the ceramics paste on the substrate. Thus, a complicated procedure of embedding an independent ceramic capacitor in the resin substrate afterward is required.

An object of the present invention is to solve the above conventional problems, thereby providing a flexible capacitor that can be easily produced at low temperature.

Means for Solving the Problems
[0013]

[0012]

To achieve the object, the capacitor of the present invention comprises a layer of a dielectric material and two electrodes sandwiching the layer, and is characterized in that the dielectric material layer contains a metal microparticle and/or an organic charge trapping material (which may be referred to as metal microparticle, etc. hereinafter) in an organic insulating material, and the metal microparticle, etc. has an ionization potential and an electron affinity at an energy level between the ionization potential and the electron affinity of the organic insulating material.

In the capacitor of the invention, once the metal

microparticle, etc. is charged by applying voltage, the charge is trapped in the metal microparticle, etc. due to the energy level relation to the organic insulating material. The trapped charge acts in the same manner as dielectric polarization in the dielectric material, so that extremely large dielectric constant can be obtained practically even when the organic insulating material has a small dielectric constant.

The capacitor can be produced at room temperature by a simple method such as a vacuum deposition method and a spin coating method, and has flexibility, characteristic of organic materials.

In the invention, it is preferred that the organic insulating material is selected from 2-amino-4,5-imidazole dicyanate, quinomethane compounds, triphenylamine compounds, and pyridone compounds, and the metal microparticle is selected from aluminum, gold, and copper particles.

In the invention, it is preferred that the organic insulating material is 2-amino-4,5-imidazole dicyanate, triphenylamine compounds, or α -NPD, and the organic charge trapping material is at least one selected from the group consisting of pyridone compounds, phthalocyanine compounds, and α -6T.

[0014]

Thus, according to the invention, there are provided the

dielectric material and the capacitor comprising the layer of the dielectric material and two electrodes sandwiching the layer, and the dielectric material comprises the organic insulating material, and the metal microparticle and/or the organic charge trapping material in the organic insulating material, the metal microparticle having a work function at an energy level between the ionization potential and the electron affinity of the organic insulating material, or alternatively the metal microparticle or the organic charge trapping material having an ionization potential and an electron affinity at an energy level between the ionization potential and the electron affinity of the organic insulating material.

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According to the invention, there is further provided a method for producing a capacitor comprising the steps of forming an electrode thin film, applying a liquid mixture containing an organic insulating material, and a metal microparticle and/or an organic charge trapping material to the formed electrode thin film, followed by drying, and forming an electrode thin film on the dried coated film.

Furthermore, in the invention, there is provided a method for producing a capacitor, comprising the steps of forming an electrode thin film, codepositing an organic insulating material, and a metal microparticle and/or an organic charge trapping material on the formed electrode thin film, and

forming an electrode thin film on the codeposited film.

Advantage of the Invention [0015]

As described in detail below, according to the present invention, there is provided a capacitor capable of showing high relative dielectric constant and large capacity even in the case of using an organic insulating material with a low relative dielectric constant. Further, the capacitor is flexible and can be produced at low temperature near room temperature, and thereby can be suitably used in various places such as printed boards and integrated circuits.

Best Mode for Carrying Out the Invention [0016]

The present invention will be described in detail below with reference to the drawings.

Fig. 1 is a schematic cross-sectional view showing one embodiment of the capacitor of the invention.

As shown in Fig. 1, this capacitor has such a structure that an electrode layer 21a, a dielectric material layer 30 of the organic insulating material, which contains the metal microparticle, etc., and an electrode layer 21b are stacked in this order on a substrate 10.

[0017]

The substrate 10 is preferably a glass substrate or a film substrate of polyimide, etc. though not particularly restrictive.

[0018]

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The materials for the electrode layers 21a, 21b are not particularly limited and may be appropriately selected from metal materials such as aluminum, gold, silver, nickel, and iron, inorganic materials such as ITO and carbon, organic conjugated materials, organic materials such as liquid crystals, semiconductor materials such as silicon, etc.

In the capacitor of the invention, the dielectric material layer 30 is constituted by an ultrathin organic film. The producing processes thereof are carried out at a low temperature of 100°C or lower, and the materials are flexible, so that the problems of breaking, peeling, contamination, etc. are hardly caused unlike conventional ceramic materials.

The dielectric material layer 30 contains the metal microparticle, etc. in the organic insulating material, and is characterized in that the work function of the metal microparticle, or the ionization potential and the electron affinity of the metal microparticle, etc. is at an energy level between the ionization potential and the electron affinity of the organic insulating material.

The work function is the amount of minimum work required to extract an electron from a solid into vacuum.

The ionization potential is an energy required to remove one electron from a neutral atom, an ion, or a molecule, and an energy for removing one outermost electron in vacuum is defined as a first ionization potential, an energy required to remove another electron from the resultant monovalent positive ion is defined as a second ionization potential, and an energy for removing the third or fourth electron is defined as a third or fourth ionization potential. Thus, a smaller ionization potential means that it is converted to a positive ion more easily. In the invention, the first ionization potential is an object for consideration.

The electron affinity is an energy released when one electron is added to an atom, a molecule, or a negative ion.
[0021]

The ionization potential of the dielectric material layer may be measured easily by photoemission spectroscopy (e.g. AC-2 manufactured by Riken Keiki Co., Ltd.) in atmosphere, etc. The electron affinity is generally obtained by measuring an optical band gap from optical absorption spectra, and by adding it to the ionization potential.

The ionization potential and the electron affinity of a single atom of each metal material are obtained as a measured value or a calculated value as shown in Table 1. When a metal

material has a sufficiently large size (in the bulk state), these values are observed as the work function, which can be easily measured by the above photoemission spectroscopy in atmosphere, etc., and those of various materials have been obtained. On the contrary, it is known that when the metal material is a fine particle having nm size as in the invention, the values depend on the particle size. Thus, the ionization potential and the electron affinity of the material in the state of fine particle are intermediate values between the ionization potential and the electron affinity of the single atom and the work function of the bulk state. It is known that these values are fundamentally well associated with the fine particle size while the values are slightly affected by polarization in the material of the dielectric layer, and data of various metal materials have been obtained. For example, references include Clusters of Atoms and Molecules, edited by Hellmut Haberland, Spring-Verlag, Berlin, 1994.

Thus, in the most simple model, the relations of the ionization potential IP and the electron affinity EA to the work function WF and the fine particle diameter R (nm) are represented by the following equations:

$$IP = WF + A / R \qquad (I)$$

$$EA = WF - B / R \qquad (II)$$

wherein $A = 3e^2/8$, $B = 5e^2/8$, and e is an electron charge.

Therefore, the ionization potential and electron

affinity can be approximately estimated by observing the fine particle size.

[0022]

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The organic insulating material is not particularly restricted, and may be selected from 2-amino-4,5-imidazole dicyanate, quinomethane compounds, triphenylamine compounds, pyridone compounds, polystyrenes, polyvinyl carbazoles, α-NPD (N,N'-di(naphthalene-1-yl)-N,N'-diphenylbenzidine), TPD (N,N'-bis(3-methylphenyl)-N,N'-diphenylbenzidine), Alq3 (tris-(8-hydroxyquinolinato) aluminum), CBP (4,4'-bis(carbazole-9-yl)-biphenyl), etc.

The average particle diameter of the metal microparticle is not particularly restricted, and is preferably 2 to 100 nm in the deposition method and 1 to 50 nm in the application method from the viewpoint of producing and dispersing the metal microparticle used.

The organic charge trapping material is required to have an ionization potential smaller than that of the organic insulating material and an electron affinity larger than that of the organic insulating material, and thereby naturally has an energy gap smaller than that of the organic insulating material. The energy gap of the organic charge trapping material is preferably 2 eV or less while it depends on the organic insulating material to be combined therewith.

More specifically, the organic charge trapping material

is preferably selected from pyridone compounds, phthalocyanine compounds, thiophene compounds typified by α -6T (α -sexithiophene), acene compounds typified by pentacene, etc.

Among the above materials, in the embodiment of using the metal microparticle, it is particularly preferred that the organic insulating material is 2-amino-4,5-imidazole dicyanate, quinomethane compounds, triphenylamine compounds, or pyridone compounds, and the metal microparticle is at least one material selected from the group consisting of aluminum, gold, and copper.

Among the above materials, in the embodiment of using the organic charge trapping material, it is particularly preferred that the organic insulating material is 2-amino-4,5-imidazole dicyanate, triphenylamine compounds, or α -NPD, and the organic charge trapping material is at least one material selected from the group consisting of pyridone compounds, phthalocyanine compounds, and α -6T.

The structural formula of 2-amino-4,5-imidazole dicyanate is shown below.

[0023]

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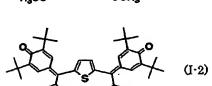
[Chemical Formula 1]

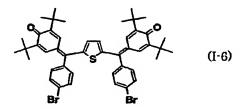
[0024]

The quinomethane compounds include ones compounds by the following formulae though not particularly restrictive.

[Chemical Formula 2]

H₃CO OCH₃





[0025]

Further, the triphenylamine compounds include compounds represented by the following formulae.

[Chemical Formula 3]

(1-18)

(1-21)

(1-21)

(1-21)

(1-22)

(1-23)

(1-24)

(1-26)

(1-26)

(1-27)

(1-34) (1-28) (1 - 29) (1 - 30) (1-37)(i -38) (1-39)

[0026]

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The pyridone compounds include compounds represented by the following formulae.

[Chemical Formula 4]

O-CH-CH-CH-CHI3 0=CH-CH=(N-CH₃

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[0027]

The phthalocyanine compounds include copper phthalocyanine, lead phthalocyanine, zinc phthalocyanine, aluminum phthalocyanine, iron phthalocyanine, cobalt phthalocyanine, tin phthalocyanine, titanyl phthalocyanine, and metal-free phthalocyanine.

[0028]

Table 1 shows the work functions, ionization potentials, electron affinities of examples of the metal microparticle, etc. and the organic insulating material.

As shown in Table 1, each of aluminum, gold, and copper has a work function at an energy level between the ionization potential and the electron affinity of 2-amino-4,5-imidazole dicyanate, whereby the dielectric constant can be effectively improved by accumulating charges in the metal microparticle. Further, by using the metal microparticle, which has such a particle diameter that IP and EP calculated from the equations (I) and (II) with the work function WF shown in Table 1 are between the ionization potential and electron affinity of the organic insulating material, the dielectric constant can be effectively improved by accumulating charges in the metal microparticle. Furthermore, as shown in Table 1, each of the pyridone compounds, the phthalocyanine compounds, and α -6T has an ionization potential and an electron affinity at energy levels between those of 2-amino-4,5-imidazole dicyanate,

whereby the dielectric constant can be effectively improved by accumulating charges in the organic charge trapping material.

[0029]

[Table 1]

		Ionization	Electron	Work
		potential	affinity	function
		(eV)	(eV)	(eV)
Organic	2-Amino-4,5-imidazole dicyanate	5.60 *1	1.82 *1	
insulating	Quinomethane compound A (see Example 5)	6.03 *1	4.08 *1	
material	Quinomethane compound B (see Example 6)	6.01 *1	4.06 *1	
	Triphenylamine compound C (see Example 7)	5.41 *1	2.97 *1	
	Triphenylamine compound D (see Example 8)	5.22 *1	2.81 *1	
	Pyridone compound E (see Example 9)	4.93 *1	3.06 *1	
	Triphenylamine compound G (see Example 15)	5.5 *1	2.6 *1	
	α-NPD	5.5	2.4	
	TPD	5.5	2.4	
	Alq3	5.8	3.1	
	CBP	6.0	2.9	
Metal	Aluminum	5.986 *2	0.441 *3	4.24 *4
microparticle	Gold	9.225 *2	0.441 *3	5.1 *4
	Copper	7.726 *2	1.228 *3	4.65 *4
	Potassium	4.341 *2	0.501 *3	2.8 *4
	Sodium	5.139 *2	0.548 *3	2.36 *4
	Calcium	6.113 *2	<0 *3	2.9 *4
	Magnesium	7.646 *2	<0 *3	3.66 *4
	Indium	5.786 *2	0.3 *3	4.09 *4
	Platinum	8.61 *2	2.128 *3	5.64 *4
	Silver	7.576 *2	1.302 *3	4.26 *4
Organic charge	Pyridone compound E (see Example 12)	4.93 *1	3.06 *1	
trapping material		5.1	3.5	
·	Zinc phthalocyanine	5.1	3.5	
	Copper phthalocyanine	5.1	3.5	

^{*1} Values measured in the invention

[0030]

The compounding volume ratio of the metal microparticle and the organic insulating material is preferably 1:1 to 8:1. When the amount of the metal microparticle is less than 1:1,

^{*2} Kagaku Binran, Kisohen II, 1993, page 618 to 619

^{*3} Kagaku Binran, Kisohen II, 1993, page 629

^{*4} Kagaku Binran, Kisohen II, 1993, page 489

the dielectric constant is too small and the desired properties are not obtained in some cases. When the amount of the metal microparticle is more than 8:1, the metal microparticles often come into contact with each other, so that the dispersion effect is not obtained and the particles are short-circuited.

The compounding volume ratio of the organic charge trapping material and the organic insulating material is preferably 1:100 to 1:1. When the amount of the organic charge trapping material is less than 1:100, the dielectric constant is too small and the desired properties are not obtained in some cases. When the amount of the organic charge trapping material is more than 1:1, the organic charge trapping materials often come into contact with each other, so that the dispersion effect is not obtained and the materials are short-circuited.

In the embodiment of dispersing the metal microparticle, etc. in the organic insulating material, it is preferred that the metal microparticle, etc. is uniformly dispersed. This is because in a nonuniform dispersion, the concentration of the metal microparticle, etc. is locally increased, and it is likely that the desired dispersion effect is not obtained due to contact of the metal microparticles, etc. with each other. [0031]

It is preferred that the electrode layer 21a, the dielectric material layer 30, and the electrode layer 21b are

formed on the substrate 10 as thin films in this order.

The thin films of the electrode layers 21a and 21b are preferably formed by a known method such as vacuum deposition though the method of forming them is not particularly restricted.

[0032]

The method for forming the dielectric material layer 30 is not particularly limited, and may be a method of applying at a time the organic insulating material 31 and the metal microparticle, etc. 32 mixed beforehand, a method of codepositing the organic insulating material 31 and the metal microparticle, etc. 32, a method of sandwiching a layer of the metal microparticle, etc. 32 between layers of the organic insulating material 31, in other words, thus forming the layer of the metal microparticle, etc. 32 in the organic insulating material 31 as an intermediate layer, etc.

The capacitor may have such a structure that the dielectric material layer 30 is further sandwiched between layers of the organic insulating material, in other words, the dielectric material layer 30 is formed as an intermediate layer in the organic insulating material.

[0033]

In the method of applying the liquid mixture of the organic insulating material and the metal microparticle, etc., it is preferred that methylene chloride, tetrahydrofuran,

acetonitrile, or ethyl alcohol, etc. is used as a solvent, and the metal microparticle, etc. and the organic insulating material are mixed at the above compounding volume ratio, diluted to a density of 0.3 to 3.0% by weight, and then applied. A surfactant, a resin binder, etc. may be added to the mixture if necessary. The application is preferably conducted by a spin coating method. The applied mixture is preferably dried at 70 to 110°C.

[0034]

In the deposition step, the temperature of the substrate is appropriately selected depending on the electrode material, the organic insulating material, and the metal microparticle, used. The temperature is preferably 0 to 150°C in the formation of the electrode layers 21a and 21b, and is preferably 0 to 100°C in the formation of the dielectric material layer 30.

In the method of depositing the mixture layer of the organic insulating material and the metal microparticle, etc., the vacuum degree is preferably 3×10^{-6} Torr, the speed for forming the film of the organic insulating material is preferably 0.5 to 2.0 Angstrom/s, and the speed for forming the film of the metal microparticle is preferably 0.1 to 1.0 Angstrom/s. The film forming speeds within these ranges are preferred from the viewpoints of controlling the deterioration by the deposited material and controlling the crystal form of the deposited film.

[0035]

The dielectric material layer 30 may be formed by a spin coating method, a vacuum deposition method, etc., which are common methods for forming organic thin films. In the case of particular metal microparticle of Au, Pt, Rh, Ag, etc., a diffusion method may be used such that after the organic insulating material film and the metal microparticle film are stacked, they are heat-treated to diffuse the metal in the organic film.

[0036]

The thickness of each electrode layer 21a, 21b is preferably 50 to 200 nm, and that of the dielectric material layer 30 is preferably 20 to 200 nm.
[0037]

The mechanism of the high dielectric constant of the capacitor of the invention produced by the above method is not elucidated in detail, and seems as follows. That is, once a charge is injected to the metal microparticle, etc. by tunnel injection, etc., the charge is trapped in the metal microparticle, etc. based on the energy level relation to the organic insulating material. The trapped charge acts in the same manner as dielectric polarization in the dielectric material, so that extremely large dielectric constant can be obtained practically even when the organic insulating material has a small dielectric constant.

[8800]

Thus, even in the case of using the organic insulating material inherently having a smaller dielectric constant, the material can practically act to show a high dielectric constant, to provide the capacitor having a large capacity.

Examples

[0039]

The capacitor of the present invention will be described in further detail below with reference to Examples without intention of restricting the invention.

[Example 1]

A capacitor having the structure shown in Fig. 1 was produced in the following manner.

A glass substrate was used as the substrate 10, and an aluminum thin film was formed as the electrode layer 21a by a vacuum deposition method. Then, 2-amino-4,5-imidazole dicyanate (Al292 available from Tokyo Kasei Kogyo Co., Ltd.) as the organic insulating material 31 and aluminum as the metal microparticle 32 were codeposited to form the dielectric material layer 30, and aluminum was formed thereon into a thin film as the electrode layer 21b, successively to produce a capacitor of Example 1.

The electrode layer 21a, the dielectric material layer 30, and the electrode layer 21b were formed such that the

thicknesses thereof were 100 nm, 100 nm, and 100 nm, respectively. The average particle diameter of aluminum as the metal microparticle 32 was about 25 nm. The deposition was carried out by a diffusion pump exhaust deposition apparatus under a vacuum degree of 3×10^{-6} torr. Aluminum was deposited by a resistance heating method at a film forming speed The 2-amino-4,5-imidazole dicyanate of 3 Angstrom/sec. containing aluminum as the metal microparticle was formed by a codeposition method. The codeposition was achieved by a 2-amino-4,5-imidazole method, the resistance heating dicyanate film forming speed was 2 Angstrom/sec, and the aluminum film forming speed was 1 Angstrom/sec. The layers were deposited successively in one deposition apparatus such that the sample did not come into contact with air in the deposition processes.

[0040]

[Example 2]

2-Amino-4,5-imidazole dicyanate was used as the organic insulating material 31, gold was used as the metal microparticle 32, and they were codeposited to form a film as the dielectric material layer 30. A capacitor was produced under the same conditions as Example 1 except for using the film.

[0041]

[Example 3]

2-Amino-4,5-imidazole dicyanate was used as the organic insulating material 31, copper was used as the metal microparticle 32, and they were codeposited to form a film as the dielectric material layer 30. A capacitor was produced under the same conditions as Example 1 except for using the film.

[0042]

[Example 4]

2-Amino-4,5-imidazole dicyanate was used as the organic insulating material 31, and aluminum was used as the metal microparticle 32, to form the dielectric material layer 30. Further, layers containing only 2-amino-4,5-imidazole dicyanate were disposed between the dielectric material layer 30 and each of the electrodes 21a and 21b to form such a structure that the dielectric material layer 30 was disposed as an intermediate layer in the organic insulating material. 2-amino-4,5-imidazole dicyanate layer having a thickness of 40 nm, the dielectric material layer 30 having a thickness of 20 nm, and the 2-amino-4,5-imidazole dicyanate layer having a thickness of 40 nm were stacked in this order by a vacuum deposition method to form a three-layered film. A capacitor was produced under the same conditions as Example 1 except for using the film.

[0043]

[Example 5]

The following quinomethane compound A was used as the organic insulating material 31 and aluminum was used as the metal microparticle 32 in the dielectric material layer 30, and a quinomethane compound layer having a thickness of 40 nm, the dielectric material layer having a thickness of 20 nm, and a quinomethane compound layer having a thickness of 40 nm were stacked in this order by a vacuum deposition method to form a three-layered film. A capacitor was produced under the same conditions as Example 1 except for using the film.

[0044]

[Example 6]

[Chemical Formula 5]

The following quinomethane compound B was used as the organic insulating material 31 and aluminum was used as the metal microparticle 32 in the dielectric material layer 30, and a quinomethane compound layer having a thickness of 40 nm, the dielectric material layer having a thickness of 20 nm, and a quinomethane compound layer having a thickness of 40 nm were stacked in this order by a vacuum deposition method to form

a three-layered film. A capacitor was produced under the same conditions as Example 1 except for using the film.

[Chemical Formula 6]

[0045]

[Example 7]

The following triphenylamine compound C was used as the organic insulating material 31 and aluminum was used as the metal microparticle 32 in the dielectric material layer 30, and a triphenylamine compound layer having a thickness of 40 nm, the dielectric material layer having a thickness of 20 nm, and a triphenylamine compound layer having a thickness of 40 nm were stacked in this order by a vacuum deposition method to form a three-layered film. A capacitor was produced under the same conditions as Example 1 except for using the film. [Chemical Formula 7]

[0046]

[Example 8]

The following triphenylamine compound D was used as the organic insulating material 31 and aluminum was used as the metal microparticle 32 in the dielectric material layer 30, and a triphenylamine compound layer having a thickness of 40 nm, the dielectric material layer having a thickness of 20 nm, and a triphenylamine compound layer having a thickness of 40 nm were stacked in this order by a vacuum deposition method to form a three-layered film. A capacitor was produced under the same conditions as Example 1 except for using the film. [Chemical Formula 8]

[0047]

[Example 9]

The following pyridone compound E was used as the organic insulating material 31 and aluminum was used as the metal microparticle 32 in the dielectric material layer 30, and a pyridone compound layer having a thickness of 40 nm, the dielectric material layer having a thickness of 20 nm, and a pyridone compound layer having a thickness of 40 nm were stacked in this order by a vacuum deposition method to form a

three-layered film. A capacitor was produced under the same conditions as Example 1 except for using the film.

[Chemical Formula 9]

[0048]

[Example 10]

Copper phthalocyanine was used as the organic charge trapping material 32 instead of the metal microparticle 32, the 2-amino-4,5-imidazole dicyanate film forming speed was 1 Angstrom/sec, and the copper phthalocyanine film forming speed was 0.5 Angstrom/sec. A capacitor was produced under the same conditions as Example 1 except therefor.

[0049]

[Example 11]

2-Amino-4,5-imidazole dicyanate was used as the organic insulating material 31, and copper phthalocyanine was used as the organic charge trapping material 32, to form the dielectric material layer 30. Further, layers containing only 2-amino-4,5-imidazole dicyanate were disposed between the dielectric material layer 30 and each of the electrodes 21a and 21b to form such a structure that the dielectric material layer 30 was disposed as an intermediate layer in the organic

insulating material. The 2-amino-4,5-imidazole dicyanate layer having a thickness of 30 nm, the dielectric material layer 30 having a thickness of 40 nm, and the 2-amino-4,5-imidazole dicyanate layer having a thickness of 30 nm were stacked in this order by a vacuum deposition method to form a three-layered film. A capacitor was produced under the same conditions as Example 10 except for using the film.

[0050]

[Example 12]

2-Amino-4,5-imidazole dicyanate was used as the organic insulating material 31 and the above pyridone compound E was used as the organic charge trapping material 32 in the dielectric material layer 30, and a 2-amino-4,5-imidazole dicyanate layer having a thickness of 30 nm, the dielectric material layer 30 having a thickness of 40 nm, and a 2-amino-4,5-imidazole dicyanate layer having a thickness of 30 nm were stacked in this order by a vacuum deposition method to form a three-layered film. A capacitor was produced under the same conditions as Example 11 except for using the film. [0051]

[Example 13]

2-Amino-4,5-imidazole dicyanate was used as the organic insulating material 31 and $\alpha\text{-}6T$ was used as the organic charge trapping material 32 in the dielectric material layer 30, and a 2-amino-4,5-imidazole dicyanate layer having a thickness of

30 nm, the dielectric material layer 30 having a thickness of 40 nm, and a 2-amino-4,5-imidazole dicyanate layer having a thickness of 30 nm were stacked in this order by a vacuum deposition method to form a three-layered film. A capacitor was produced under the same conditions as Example 11 except for using the film.

[0052]

[Example 14]

The following triphenylamine compound F was used as the organic insulating material 31 and copper phthalocyanine was used as the organic charge trapping material 32 in the dielectric material layer 30, and a triphenylamine compound layer having a thickness of 40 nm, the dielectric material layer having a thickness of 20 nm, and a triphenylamine compound layer having a thickness of 40 nm were stacked in this order by a vacuum deposition method to form a three-layered film. A capacitor was produced under the same conditions as Example 11 except for using the film.

[Chemical Formula 10]

[0053]

[Example 15]

The following triphenylamine compound G was used as the organic insulating material 31 and copper phthalocyanine was used as the organic charge trapping material 32 in the dielectric material layer 30, and a triphenylamine compound layer having a thickness of 40 nm, the dielectric material layer having a thickness of 20 nm, and a triphenylamine compound layer having a thickness of 40 nm were stacked in this order by a vacuum deposition method to form a three-layered film. A capacitor was produced under the same conditions as Example 11 except for using the film.

[Chemical Formula 11]

[0054]

[Example 16]

 α -NPD was used as the organic insulating material 31 and copper phthalocyanine was used as the organic charge trapping material 32 in the dielectric material layer 30, and an α -NPD layer having a thickness of 40 nm, the dielectric material layer having a thickness of 20 nm, and an α -NPD layer having a thickness of 40 nm were stacked in this order by a vacuum deposition method to form a three-layered film. A capacitor was produced under the same conditions as Example 11 except for using the film.

[0055]

[Comparative Example 1]

A glass substrate was used as the substrate 10, and by a vacuum deposition method, aluminum was formed into the electrode layer 21a, 2-amino-4,5-imidazole dicyanate was formed into the dielectric material layer, and aluminum was formed into a thin film for the electrode layer 21b, successively, to produce a capacitor of Comparative Example 1. The production conditions were equal to those of Example 1 except for not codepositing aluminum with 2-amino-4,5-imidazole dicyanate.

[0056]

[Comparative Example 2]

A capacitor of Comparative Example 2 was produced in the same manner as Comparative Example 1 except for using the quinomethane compound A of Example 5 in the dielectric material layer.

[0057]

[Comparative Example 3]

A capacitor of Comparative Example 3 was produced in the same manner as Comparative Example 1 except for using the quinomethane compound B of Example 6 in the dielectric material layer.

[0058]

[Comparative Example 4]

A capacitor of Comparative Example 4 was produced in the same manner as Comparative Example 1 except for using the triphenylamine compound C of Example 7 in the dielectric material layer.

[0059]

[Comparative Example 5]

A capacitor of Comparative Example 5 was produced in the same manner as Comparative Example 1 except for using the triphenylamine compound D of Example 8 in the dielectric material layer.

[0060]

[Comparative Example 6]

A capacitor of Comparative Example 6 was produced in the same manner as Comparative Example 1 except for using the pyridone compound E of Example 9 in the dielectric material layer.

[0061]

[Comparative Example 7]

A glass substrate was used as the substrate 10, and by a vacuum deposition method, aluminum was formed into the electrode layer 21a, 2-amino-4,5-imidazole dicyanate was formed into the dielectric material layer, and aluminum was formed into a thin film for the electrode layer 21b, successively, to produce a capacitor of Comparative Example 7. The production conditions were equal to those of Example

10 except for not codepositing copper phthalocyanine with 2-amino-4,5-imidazole dicyanate.

[0062]

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[Comparative Example 8]

A capacitor of Comparative Example 8 was produced in the same manner as Comparative Example 7 except for using the triphenylamine compound F of Example 14 in the dielectric material layer.

[0063]

[Comparative Example 9]

A capacitor of Comparative Example 9 was produced in the same manner as Comparative Example 7 except for using the triphenylamine compound G of Example 15 in the dielectric material layer.

[0064]

[Comparative Example 10]

A capacitor of Comparative Example 10 was produced in the same manner as Comparative Example 7 except for using $\alpha\text{-NPD}$ of Example 16 in the dielectric material layer.

[0065]

[Evaluation method]

The relative dielectric constants of the capacitors of Examples 1 to 16 and Comparative Examples 1 to 10 were measured at the room temperature. The relative dielectric constants were measured by an impedance analyzer YHP4192A. The measured

relative dielectric constants at 1 kHz are shown in Table 2. Further, a scanning electron microscope (SEM) photograph of the surface of the dielectric material layer of Example 1 is shown in Fig. 2.

[0066]

** 10 °

[Table 2]

	Structure of dielectric material layer				
		Metal microparticle or		Relative	
,	Functional organic material	organic charge	Film	dielectric	
		trapping material		constant	
Example 1	2-Amino-4,5-imidazole dicyanate	Aluminum	Codeposited film	156	
Example 2	2-Amino-4,5-imidazole dicyanate	Gold	Codeposited film	244	
Example 3	2-Amino-4,5-imidazole dicyanate	Copper	Codeposited film	75	
Example 4	2-Amino-4,5-imidazole dicyanate	Aluminum	Three-layered film	96	
Example 5	Quinomethane compound	Aluminum	Three-layered film	11.5	
Example 6	Quinomethane compound	Aluminum	Three-layered film	37.3	
Example 7	Triphenylamine compound	Aluminum	Three-layered film	17.3	
Example 8	Triphenylamine compound	Aluminum	Three-layered film	21.8	
Example 9	Pyridone compound	Aluminum	Three-layered film	32	
Example 10	2-Amino-4,5-imidazole dicyanate	Copper phthalocyanine		57	
Example 11	2-Amino-4,5-imidazole dicyanate	Copper phthalocyanine	Three-layered film	42	
Example 12	2-Amino-4,5-imidazole dicyanate	Pyridone compound	Three-layered film	29	
Example 13	2-Amino-4,5-imidazole dicyanate	α-6Τ	Three-layered film	35	
Example 14	Triphenylamine compound	Copper phthalocyanine	Three-layered film	17	
Example 15	Triphenylamine compound	Copper phthalocyanine	Three-layered film	26	
Example 16	α-NPD	Copper phthalocyanine	Three-layered film	65	
Comp. Ex. 1	2-Amino-4,5-imidazole dicyanate	None	Deposited film	3.6	
Comp. Ex. 2	Quinomethane compound	None	Deposited film	2.1	
Comp. Ex. 3	Quinomethane compound	None	Deposited film	2.5	
Comp. Ex. 4	Triphenylamine compound	None	Deposited film	2.7	
Comp. Ex. 5	Triphenylamine compound	None	Deposited film	3.2	
Comp. Ex. 6	Pyridone compound	None	Deposited film	3.0	
Comp. Ex. 7	2-Amino-4,5-imidazole dicyanate	None	Deposited film	3	
Comp. Ex. 8	Triphenylamine compound	None	Deposited film	3	
Comp. Ex. 9	Triphenylamine compound	None	Deposited film	4	
Comp. Ex. 10	α-NPD	None	Deposited film	3	

[0067]

As shown in Table 2, while the relative dielectric constant of 2-amino-4,5-imidazole dicyanate used as the organic insulating material 31 was measured in Comparative

Example 1, the relative dielectric constants of Examples 1 to 4 were 20 to 70 times as large as that of Comparative Example 1 despite the use of the same organic insulating material. Further, the quinomethane organic materials, triphenylamine compounds, and the pyridone compound were used as the organic insulating material 31 in Examples 5 to 9 to show the large dielectric constants, which were 5 to 20 times as large as those of Comparative Examples 2 to 6.

[8800]

Similarly, as shown in Table 2, while the relative dielectric constant of 2-amino-4,5-imidazole dicyanate used as the organic insulating material 31 was measured in Comparative Example 7, the relative dielectric constants of Examples 10 to 13 were 10 to 20 times as large as that of Comparative Example 7 despite the use of the same organic insulating material. Further, the triphenylamine compounds and α -NPD were used as the organic insulating material 31 in Examples 14 to 16 to show the large dielectric constants, which were 5 to 20 times as large as those of Comparative Examples 8 and 9.

Brief Description of the Drawings
[0069]

Fig. 1 is a schematic cross-sectional view showing one embodiment of the capacitor of the present invention.

Fig. 2 is a scanning electron microscope (SEM) photograph showing the surface of the dielectric material layer of Example 1.

Description of Reference Numerals and Signs [0070]

10: Substrate

21a, 21b: Electrode layer

30: Dielectric material layer

31: Organic insulating material

32: Metal microparticle and/or organic charge trapping material

Industrial Applicability
[0071]

According to the present invention, there is provided the capacitor capable of showing a large relative dielectric constant and large capacity even in the case of using an organic insulating material with a low relative dielectric constant. Further, the capacitor is flexible and can be produced at low temperature near room temperature, and thereby can be suitably used in various places such as printed boards and integrated circuits.